

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerd Frankowsky et al.                      Art Unit : Unknown  
Serial No. :    Examiner : Unknown  
Filed : July 29, 2003  
Title : SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING  
SEMICONDUCTOR CIRCUIT MODULES

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.

CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. EV331653782US

July 29, 2003  
Date of Deposit